



Attorney's Docket No.: 10559-075002
Intel Docket No.: P7567C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Adiletta et al.

Art Unit : 2183

Serial No.: 10/615,500

Examiner : Ellis, Richard L

Filed : 7/8/2003

Assignee : Intel Corporation

Title : PARALLEL PROCESSOR ARCHITECTURE

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF DECEMBER 23, 2005

Claim Listing/Amendments

... page 2

Remarks

... page 4

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

2/22/2006

Signature

Debbie Nast

Typed or Printed Name of Person Signing Certificate